FIG. 1

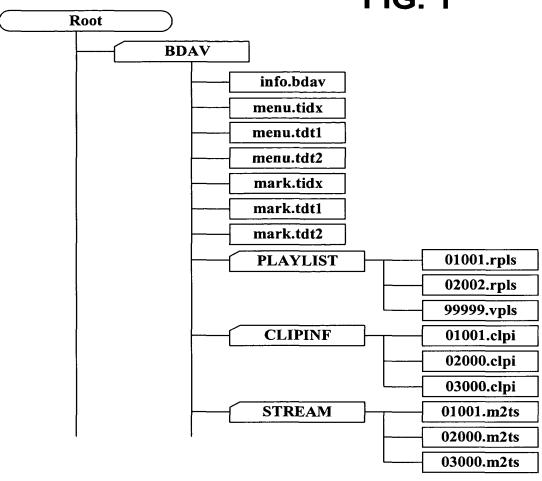


FIG. 2

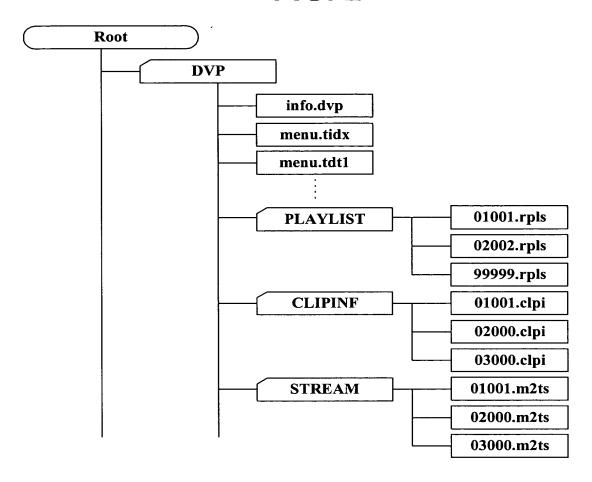


FIG. 3

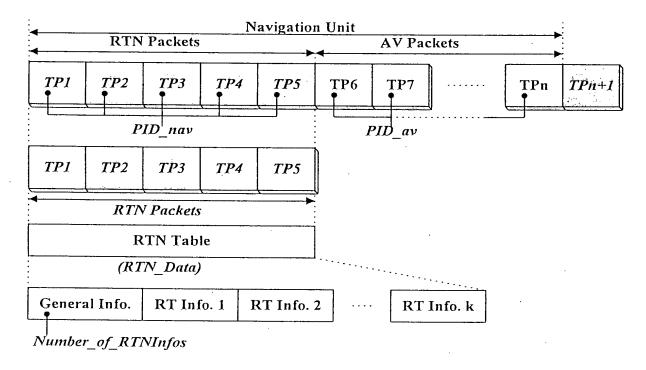


FIG. 4

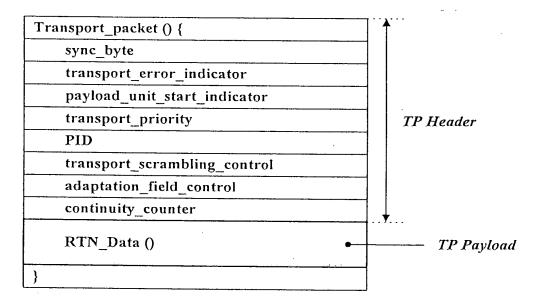
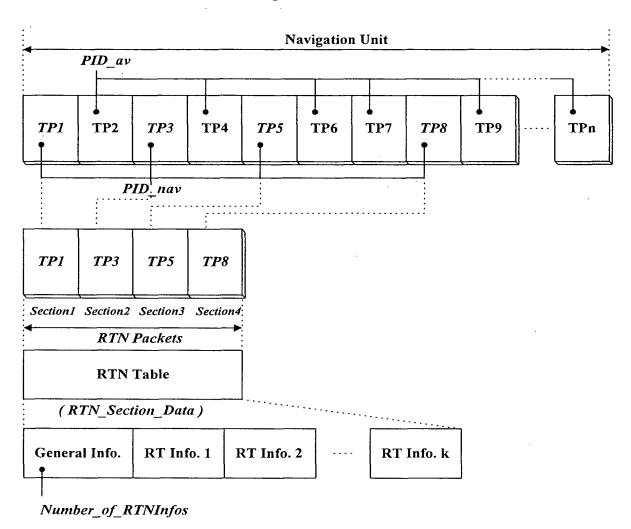
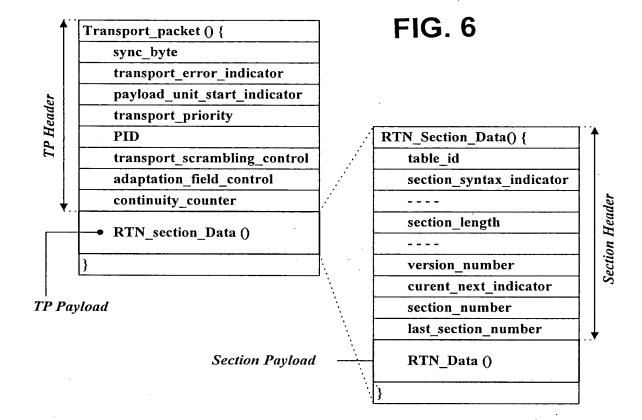


FIG. 5





Drive Source Source Depacketizer **Packetizer** Controller Demultiplexer Multiplexer 10 AV Encoder AV Decoder တ σ user interface Digital data in put AN data in put AN data output

FIG. 8

ECC Allocation Unit

TP 1	TP 2	-		TP k
			***************************************	***************************************
Section #1 (2048B)	Section #2 (2048B)	Section #3 (2048B)		
····		***************************************		
Align Unit #1 (3 x 2048B)	Align Unit #2 (3 x 2048B)	Align Unit #2 (3 x 2048B)		Align Unit #32 (3 x 2048B)
······································				***************************************
ECC #1 (64KB)	ECC #2 (64KB)	ECC #3 (64KB)	ECC #4 (64KB)	

RTN Data